

# 68. WORKSHOP ON MICROELECTRONICS

17 January 2025 // University of Applied Sciences Düsseldorf, Germany

## CALL FOR PAPERS



### Topics

- Analog, Mixed-Signal, and Digital Circuit Design
- Application-Specific Integrated Circuits (ASICs)
- Electronic Systems Design
- Embedded Systems
- FPGA/CPLD Design
- PCB Design
- Electronic Design Automation
- Microelectronics Education

### Important Dates

Submission of contributions	6 December 2024
Notification of acceptance	13 December 2024

A prize will be awarded for the best student presentation

**Steering Committee** D. Benyoucef, H.-P. Bürkle, J. Gerlach, R. Gessler, J. Giehl, E. Hennig, F. Kesel, E. Mackensen, H.-J. Ng, W. Lindermeir, C. Schick, A. Siggelkow, A. Terzis

**Program Committee** J. Giehl, E. Hennig, B. Rieß, A. Terzis **Local Organisation** Prof. Dr. B. Rieß (Hochschule Düsseldorf) **Conference Language** German and English

